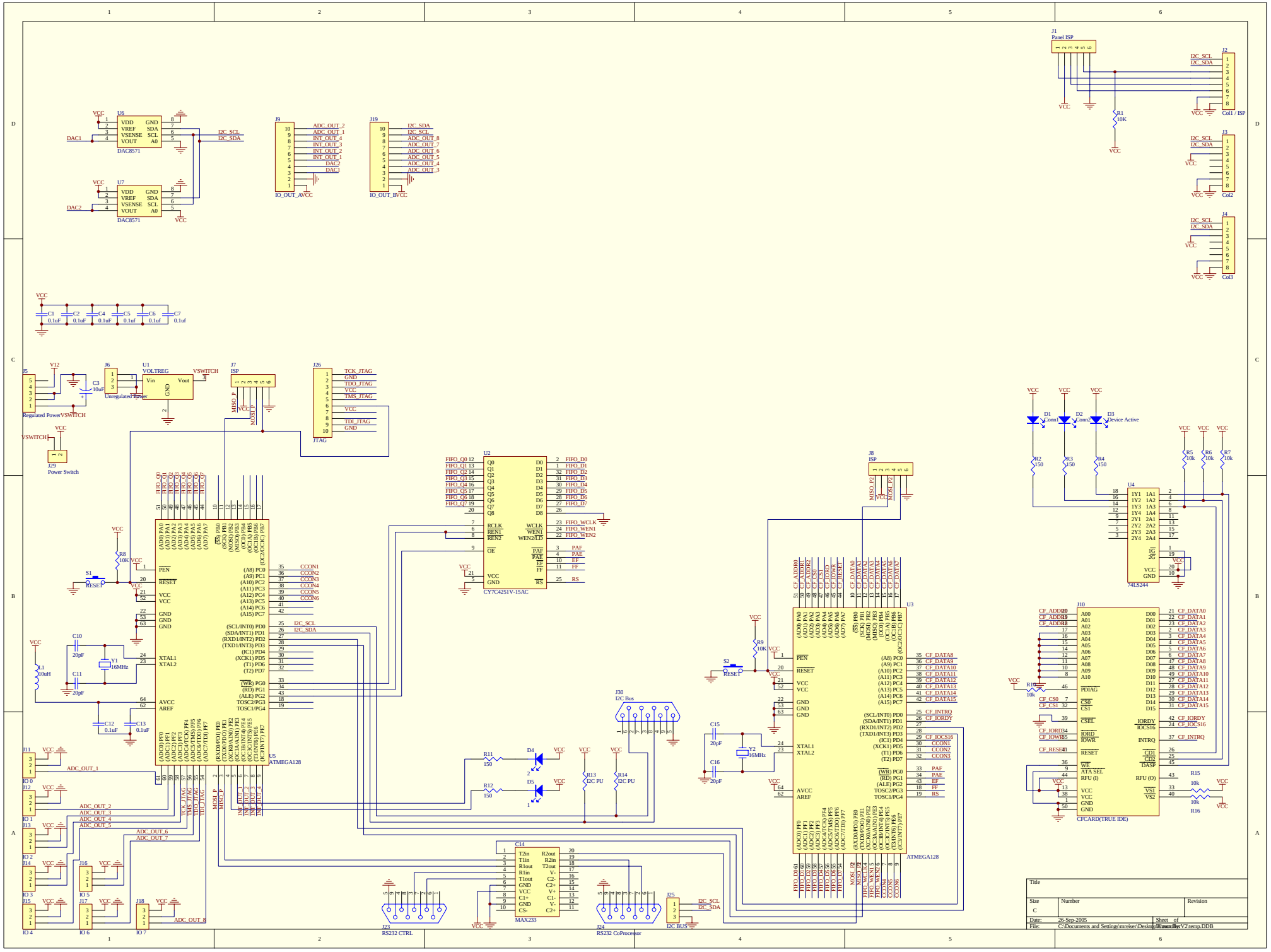
U6
DAC8571
U7
DAC8571
J9
IO_OUT_A
J19
IO_OUT_B
J1
Panel ISP
J2
Col1 / ISP
J3
Col2
J4
Col3
R1
10K
C1 0.1uF
C2 0.1uF
C4 0.1uF
C5 0.1uf
C6 0.1uf
C7 0.1uf
J5
Regulated Power
U1
VOLTREG
Unregulated Power
J6
J7
ISP
J26
JTAG
J29
Power Switch
U2
CY7C4251V-15AC
U5
ATMEGA128
J8
ISP
U3
ATMEGA128
U4
74LS244
J10
CFCARD(TRUE IDE)
J30
I2C Bus
J23
RS232 CTRL
C14
MAX233
J24
RS232 CoProcessor
J25
I2C BUS
D1 Conn1
D2 Conn2
D3 Device Active
R2 150
R3 150
R4 150
R5 10k
R6 10k
R7 10k
R8 10K
R9 10K
R10 10k
R11 150
R12 150
R13 I2C PU
R14 I2C PU
R15 10k
R16 10k
S1 RESET
S2 RESET
Y1 16MHz
Y2 16MHz
L1 10uH
C10 20pF
C11 20pF
C12 0.1uF
C13 0.1uF
C15 20pF
C16 20pF
Title
Size C
Number
Revision
Date: 26-Sep-2005
Sheet of
File: C:\Documents and Settings\mreiser\Desktop\Eurorack\EurorackV2\temp.DDB